

Amendments to the Specification:

Please replace Paragraph [0006] with the following amended paragraph:

Paragraph [0006]: Fig.2 is a block diagram of a first IDE/SATA architecture 200 having a single peripheral device 204 according to the prior art. The first IDE/SATA architecture
5 200 includes the peripheral device 204 electrically connected to a host 202 using a port 216 on an IDE or SATA channel 206. The host 202 could be a personal computer system, a central processing unit (CPU) of an embedded system, or another device that needs to access the peripheral device 204. In the first IDE/SATA architecture 200 shown in Fig.2, the peripheral device 204 is an optical storage device and includes a controller 208, buffer
10 memory 214, an optical storage medium 212, and an optical pick-up 210. The optical pick-up 210 has a plurality of sensors that follow a track on the optical storage medium 212. According to signals received by the sensors, which detect reflected light from a laser that reflects off pits on the optical storage medium 212, a received signal is obtained. Afterwards the received signal undergoes a decoding process to be converted into
15 received bits. For writing to the optical storage medium 212, a reverse operation is performed with the laser burning the pits into the optical storage medium 212 corresponding to a transmit signal encoded according to a set of transmitted bits. As the detailed operation of optical storage devices is well known in the prior art, further description is hereby omitted. In Fig.2, the controller 208 on the first peripheral device
20 202 acts as the master of the IDE/SATA channel 206. In this way, the host 202 can access the first peripheral device 204 using the IDE/SATA channel 206 via port 216.

Please replace Paragraph [0035] with the following amended paragraph:

In Paragraph [0035]: Fig.10 is a diagram showing the interaction between driver software running on the host and firmware software running on the controller of Fig.6. The host
25 602 runs an operating system (OS) in addition to a vendor driver corresponding to the controller 612. The controller 612 has firmware code 714, which includes both a default

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ATAPI driver code 800 and code specifying vendor specific functions 802. The vendor driver running on the host ~~612~~ 602 includes an (optional) scheduler 804 and a device driver 806. The OS includes a default optical device driver 808 and default removable media driver 810.